

Notice of Allowability	Application No.	Applicant(s)	
	09/473,558	CHIOU, BIN CHI	
	Examiner	Art Unit	
	Raj K. Jain	2616 [°]	
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	lication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to 10/20/06.		•	
2. X The allowed claim(s) is/are 1-11,13-15 renumbered 1-14.			
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	 5. Notice of Informal Patent Application 6. Interview Summary (PTO-413), Paper No./Mail Date		
	8. ⊠ Examiner's Stateme9. □ Other	8. ☑ Examiner's Statement of Reasons for Allowance9. ☐ Other	
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Allowable Subject Matter

The following is an examiner's statement of reasons for allowance:

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Claim 1 is allowed.

The prior art discloses a switch with plurality of ports and plurality of memory storage devices for storing of source and destination address and associated messages.

The prior art however fails to disclose a switch device, connected to plurality of network ports with a first and second memory control devices, for creating a source address and associated messages of said network ports by the first memory device, creating a destination address and associated messages of said network ports for network packets in accordance with the contents of said first memory device, and managing the contents of said first memory device; and a memory management device, connected to said plurality of network ports and said second memory control device, for managing the contents of said second memory device.

Claim 9, is allowed.

The prior art discloses a switch with plurality of ports having a routing table for recording of source and destination addresses and a route learning device and route selecting devices that respond to the destination address within a packet header.

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The prior art fails to disclose or suggest providing an arbitrating device for arbitrating between route learning and selection processing order for packets received from the plurality of network ports.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Raj K. Jain whose telephone number is 571-272-3145. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi Pham can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

RJ January 3, 2007

CHI PHAM
SUPERVISORY PATENT EXAMINER

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